



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#8/A
12-13-03
[Signature]

In re Application

PATENT APPLICATION

Inventors: Adrian J. Isles

Art Unit: 2123

Application No.: 09/586,191

Examiner: Sharon, Ayal I.

Filed: June 2, 2000

Customer No. 28554

Title: CIRCUIT-LEVEL MEMORY AND
COMBINATIONAL BLOCK MODELING

RECEIVED

DEC 12 2003

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 2, 2003.

Technology Center 2100

[Signature]

David E. Cromer, Reg. No. 54,768
Signature Date: December 2, 2003

RESPONSE A TO OFFICE ACTION UNDER 37 C.F.R. §1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This RESPONSE is in reply to the Office Action mailed July 2, 2003.

AMENDMENTS TO THE SPECIFICATION begin on Page 2 of this RESPONSE.

AMENDMENTS TO THE CLAIMS are reflected in the listing of claims which begins on page 3 of this RESPONSE.

REMARKS begin on Page 18 of this RESPONSE.

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